

FIG.

FIG.3

GRADATION INTEGRATION METHOD (2→3)

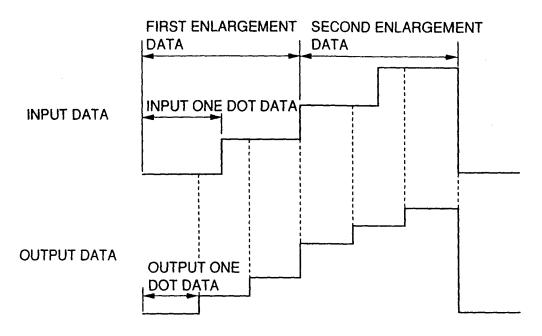


FIG.4

SIMPLE ENLARGEMENT METHOD (2→3)

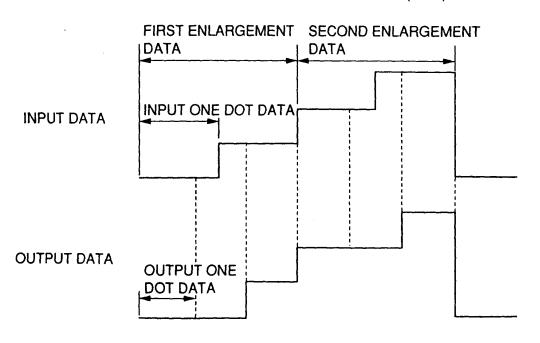
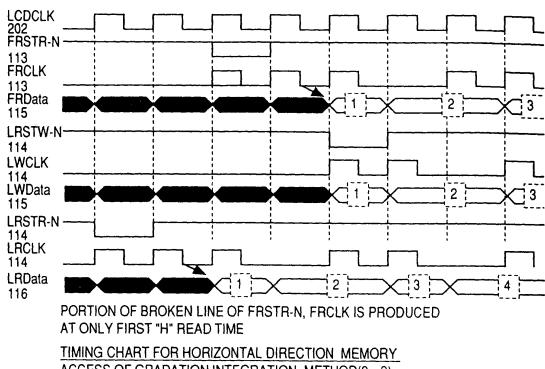
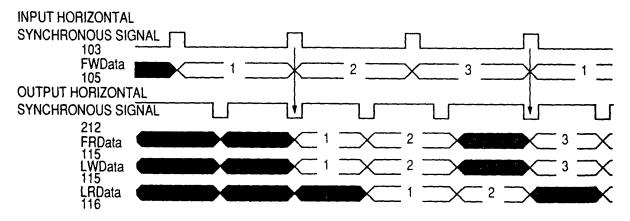


FIG.5

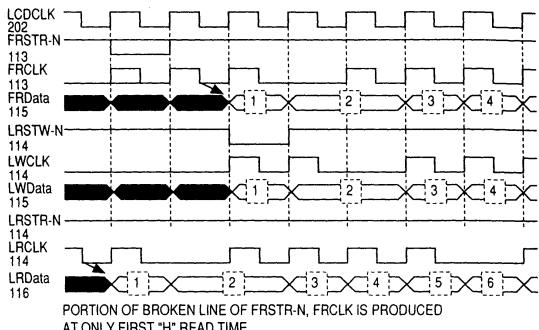


ACCESS OF GRADATION INTEGRATION METHOD(2→3)



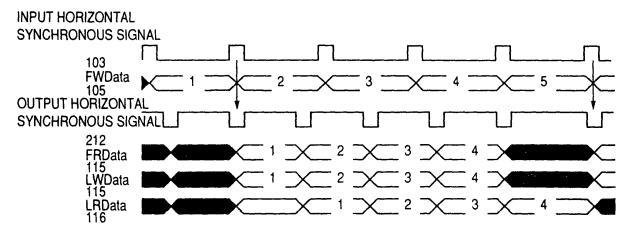
TIMING CHART FOR VERTICAL DIRECTION MEMORY ACCESS OF GRADATION INTEGRATION METHOD(2→3)

FIG.6



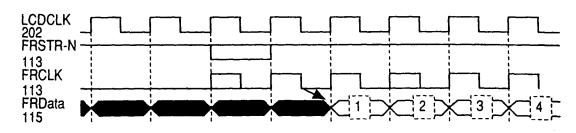
AT ONLY FIRST "H" READ TIME

TIMING CHART FOR HORIZONTAL DIRECTION MEMORY ACCESS OF GRADATION INTEGRATION METHOD(4→5)



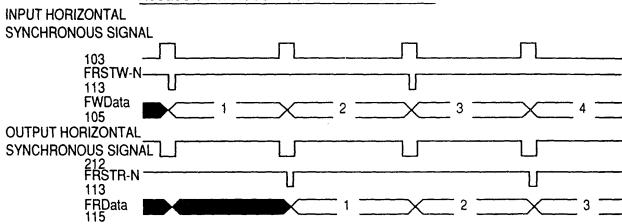
TIMING CHART FOR VERTICAL DIRECTION MEMORY ACCESS OF GRADATION INTEGRATION METHOD(4→5)

FIG.7



PORTION OF BROKEN LINE OF FRSTR-N, FRCLK IS PRODUCED AT ONLY FIRST "H" READ TIME

TIMING CHART FOR HORIZONTAL DIRECTION MEMORY ACCESS OF THROUGH MODE WITH MEMORY



TIMING CHART FOR VERTICAL DIRECTION MEMORY ACCESS OF THROUGH MODE WITH MEMORY

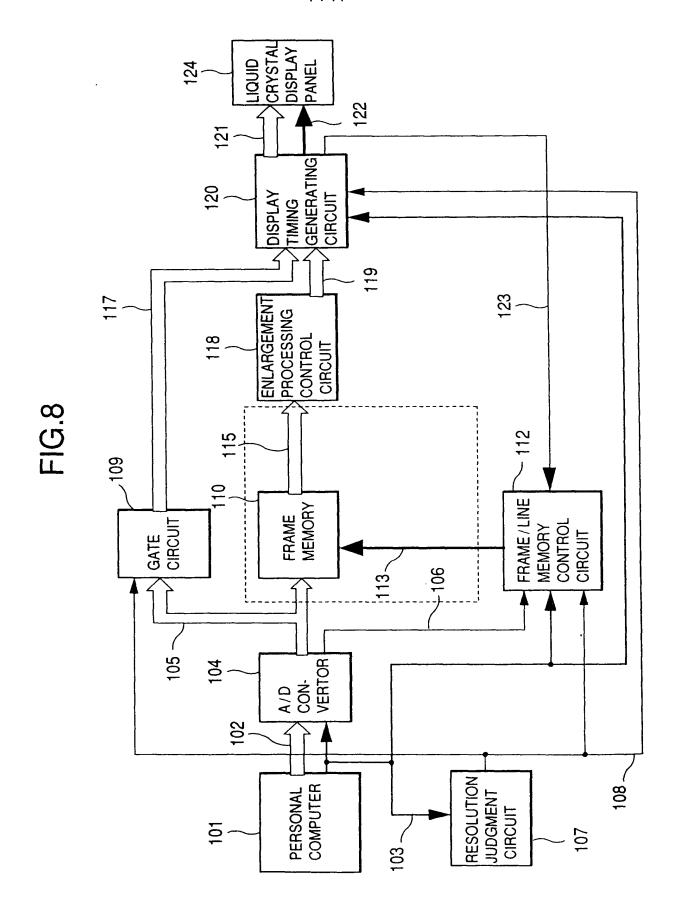
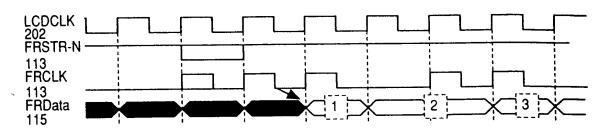
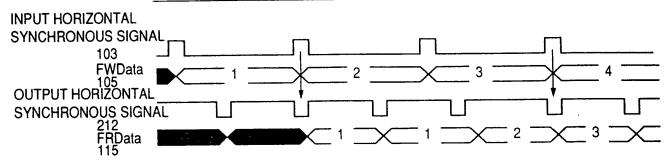


FIG.9



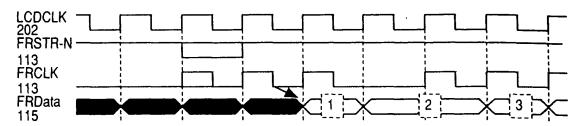
PORTION OF BROKEN LINE OF FRSTR-N, FRCLK IS PRODUCED AT ONLY FIRST "H" READ TIME

TIMING CHART FOR HORIZONTAL DIRECTION MEMORY ACCESS OF SIMPLE ENLARGEMENT(2→3)



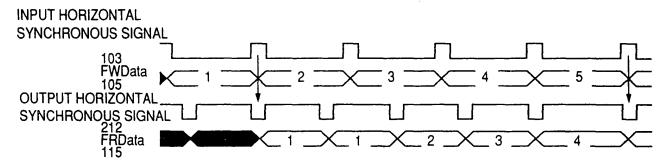
TIMING CHART FOR VERTICAL DIRECTION MEMORY ACCESS OF SIMPLE ENLARGEMENT(2→3)

FIG.10



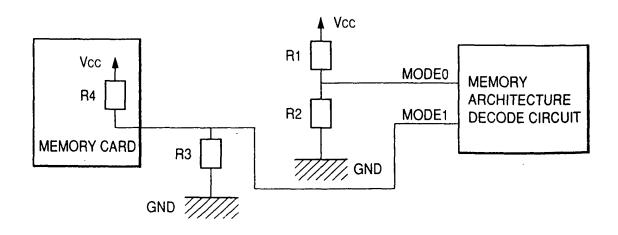
PORTION OF BROKEN LINE OF FRSTR-N, FRCLK IS PRODUCED AT ONLY FIRST "H" READ TIME

TIMING CHART FOR HORIZONTAL DIRECTION MEMORY ACCESS OF SIMPLE ENLARGEMENT(4-5)



TIMING CHART FOR VERTICAL DIRECTION MEMORY ACCESS OF SIMPLE ENLARGEMENT(4→5)

FIG.11



ENLARGED DISPLAY

CONTROL CIRCUIT

SIGNAL DISCRIMINANT CIRCUIT

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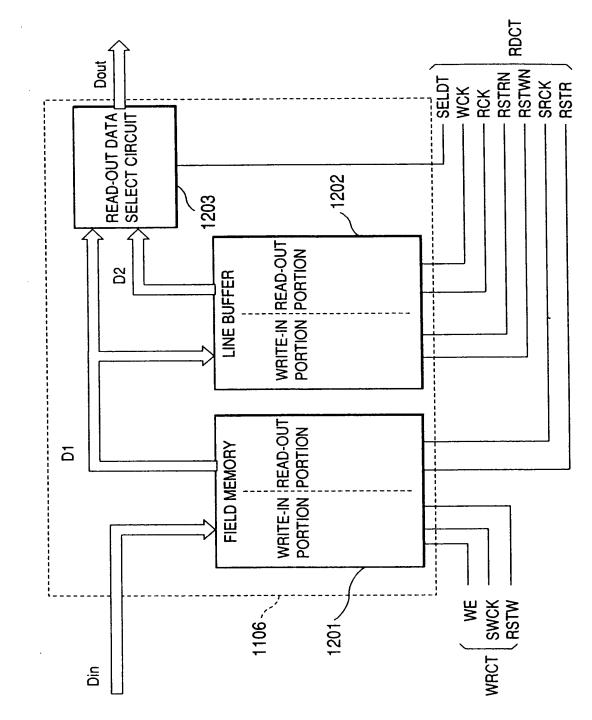


FIG.13